

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,841,432 B1
DATED : January 11, 2005
INVENTOR(S) : Yasuhiko Takemura et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 12,

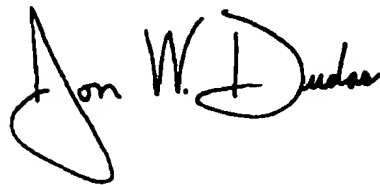
Line 67, after "and" please insert -- , for electrically isolating said plurality of thin film transistors from one another, --.

Column 13,

Line 32, after "and" please insert -- , for electrically isolating said plurality of thin film transistors from one another, --.

Signed and Sealed this

Fifteenth Day of November, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D".

JON W. DUDAS
Director of the United States Patent and Trademark Office